

Atty Docket No. 4860.P2438

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:) Examiner: A. Caschera
Sara Biyabani)
Serial No. 09/589,621) Art Unit: 2628
Filed: June 7, 2000)
For: DECOUPLING A COLOR) Confirmation No. 8620
BUFFER FROM MAIN)
MEMORY)

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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Sir:

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 2/20, 2007



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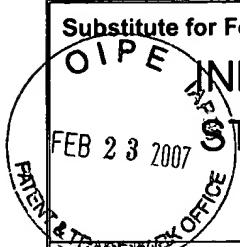
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of

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Attorney Docket No.

04860.P243

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Filing Date 06/07/2000

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Exmnr Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)				
		EP 0525986	A2	02/03/1993	Priem, Curtis et al.	

Examiner Signature		Date Considered	
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Complete if Known

Application Number	09/589,621
Filing Date	06/07/2000
First Named Inventor:	Sara Ruhina Biyabani
Art Unit	2628
Examiner Name	Caschera, Antonio A.

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		JUPITERMEDIA CORPORATION. "Integrated Video: Breaking the last PC Taboo," 802.11 Planet Conference & Expo World Tour, downloaded from the Internet at: www.cpuplanet.com/features/article.php/1566521, © 2003 Jupitermedia Corporation (2 pages).	
		KOCSIS, DAVID. "Frame-buffer wars: new directions in PC graphics," <u>EDN Access for Design, by Design</u> , May 9, 1996 (10 pages).	
		SILICON GRAPHICS, INC. "O ² Unified Memory Architecture," <u>Silicon Graphics, Inc.</u> , 1997 (pp. 1-6).	
		WILLIAMS, IAN. "An Explanation of the Advantages Afforded by QED RM5200 and MIPS R12000 CPUs in O2," SGI White Paper, 1999 (pp. 1-41).	

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